CLAIMS:

1. A display panel comprising:

display image generating means for generating a display image according to inputted display data; and

display image separating means for separating the display image, at one time or in a time division manner, according to a plurality of viewpoints,

the display image generating means being an active matrix type display panel, aperture sections in each pixel pattern of the display panel having a width set so as to satisfy the following inequality,

$0 <$ (minimum width of the aperture sections in the pixel) / (maximum width of the aperture sections in the pixel) ≤ 0.037, or

$0.130 \leq$ (minimum width of the aperture sections in the pixel) / (maximum width of the aperture sections in the pixel) < 1.

2. The display panel according to claim 1, wherein the width of the aperture sections in the pixel pattern of the active matrix type display panel is set to satisfy the following inequality,

$0 <$ (minimum width of the aperture sections in the pixel) / (maximum width of the aperture sections in the pixel) ≤ 0.037,

0.148 ≤ (minimum width of the aperture sections in the pixel) / (maximum width of the aperture sections in the pixel) ≤ 0.185, or

0.296 ≤ (minimum width of the aperture sections in the pixel) / (maximum width of the aperture sections in the pixel) < 1.

3. A display panel comprising:

display image generating means for generating a display image according to inputted display data; and

display image separating means for separating the display image, at one time or in a time division manner, according to a plurality of viewpoints,

the display image generating means being an active matrix type display panel, aperture sections in each pixel pattern of the display panel having a width set so as not to fall within a range specified by the following inequality:

2 μm < (minimum width of the aperture sections in the pixel) < 7 μm.

4. The display panel according to claim 3, wherein the width of the aperture sections in the pixel pattern of the active matrix type display panel is set so as not to fall within a range specified by the following inequalities:

2 μm < (minimum width of the aperture sections in

the pixel) < 8 μm, and

10 μm < (minimum width of the aperture sections in the pixel) < 16 μm.

5. A display panel comprising:

display image generating means for generating a display image according to inputted display data; and

display image separating means for separating the display image, at one time or in a time division manner, according to a plurality of viewpoints,

the display image generating means being an active matrix type display panel, a light shielding film being provided to avoid that the light enters aperture sections, in each pixel pattern of the display panel, having a narrow gap.

6. The display panel according to claim 5, wherein the width of the aperture sections shielded by the light-shielding film is set to satisfy the following inequality:

0.037 < (minimum width of the aperture sections in the pixel) / (maximum width of the aperture sections in the pixel) < 0.130.

7. The display panel according to claim 5, wherein

the width of the aperture sections shielded by the light-shielding film is set to satisfy the following inequality:

2 μm < (minimum width of the aperture sections in the pixel) < 7 μm.

8. The display panel according to any one of claims 1, 3, and 5,

wherein the active matrix type display panel includes: an auxiliary capacitor in the pixel; and auxiliary capacity wiring constituting the auxiliary capacitor,

the auxiliary capacity wiring having a narrow line width at an intersection with a source line and having a broad line width in a pixel pattern.

9. The display panel according to any one of claims 1, 3, and 5, wherein the active matrix type display panel is a TFT (thin film transistor) driven type display panel.

10. A display apparatus comprising the display panel according to any one of claims 1 through 8.